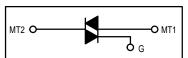
Advance Information

TRIACS

Silicon Bidirectional Thyristors

Designed for high performance full—wave ac control applications where high noise immunity and commutating di/dt are required.

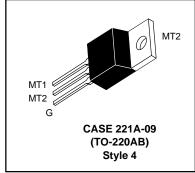
- Blocking Voltage to 800 Volts
- On–State Current Rating of 12 Amperes RMS at 70°C
- Uniform Gate Trigger currents in Three Modes
- High Immunity to dv/dt 250 V/μs minimum at 125°C
- High Commutating di/dt 6.5 A/ms minimum at 125°C
- Industry Standard TO-220 AB Package
- High Surge Current Capability 120 Amperes



MAC12 SERIES*

*Motorola preferred devices

TRIACS 12 AMPERES RMS 400 thru 800 VOLTS



MAXIMUM RATINGS (T_{.J} = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Peak Repetitive Off–State Voltage ⁽¹⁾ (T _J = -40 to 125°C, Sine Wave, 50 to 60 Hz, Gate Open)	MAC12D MAC12M MAC12N	VDRM	400 600 800	Volts
On–State RMS Current (Full Cycle Sine Wave, 60 Hz, T _C = 70°C)		lT(RMS)	12	А
Peak Non-repetitive Surge Current (One Full Cycle, 60 Hz, T _J = 125°C)		ITSM	100	А
Circuit Fusing Consideration (t = 8.3 ms)		I ² t	41	A ² sec
Peak Gate Power (Pulse Width ≤1.0 μs, T _C = 80°C)		P _{GM}	16	Watts
Average Gate Power (t = 8.3 ms, T _C = 80°C)		P _{G(AV)}	0.35	Watts
Operating Junction Temperature Range		TJ	-40 to +125	°C
Storage Temperature Range		T _{stg}	-40 to +150	°C

THERMAL CHARACTERISTICS

Thermal Resistance — Junction to Case — Junction to Ambient	$R_{ heta JC} \ R_{ heta JA}$	2.2 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 10 Seconds	TL	260	°C

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Peak Repetitive Blocking Current (V _D = Rated V _{DRM} , Gate Open)	T _J = 25°C T _J = 125°C	IDRM	_	_	0.01 2.0	mA

⁽¹⁾ VDRM and VRRM for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

This document contains information on a new product. Specifications and information herein are subject to change without notice.

Preferred devices are Motorola recommended choices for future use and best overall value.

REV 2



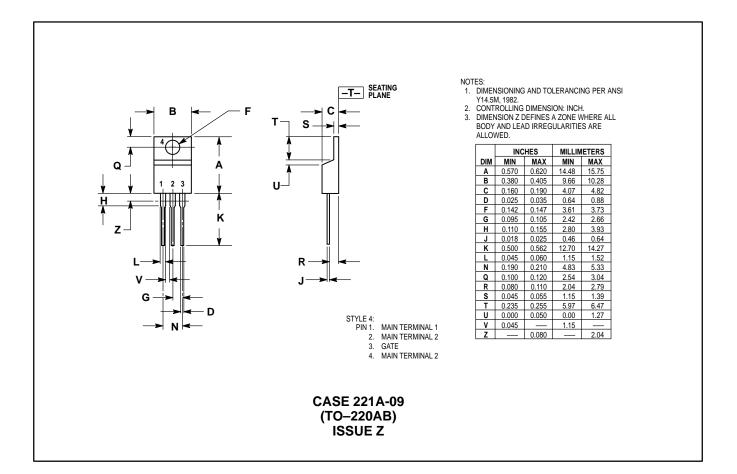
MAC12 SERIES

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS					
Peak On–State Voltage* (I _{TM} = ±17 A)	V _{TM}	_	_	1.85	Volts
Continuous Gate Trigger Current (V_D = 12 V, R_L = 100 Ω) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)	I _{GT}	5.0 5.0 5.0	13 16 18	35 35 35	mA
Hold Current (V _D = 12 V, Gate Open, Initiating Current = ±150 mA)	lн	_	20	40	mA
Latch Current ($V_D = 24 \text{ V}$, $I_G = 35 \text{ mA}$) MT2(+), G(+); MT2(-), G(-) MT2(+), G(-)	ΙL	=	20 30	50 80	mA
Gate Trigger Voltage (V _D = 12 V, R _L = 100 Ω) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)	Vgт	0.5 0.5 0.5	0.69 0.77 0.72	1.5 1.5 1.5	Volts
DYNAMIC CHARACTERISTICS					
Rate of Change of Commutating Current* ($V_D = 400 \text{ V}$, ITM = 4.4A, Commutating dv/dt = 18 V/ μ s, Gate Open, $T_J = 125^{\circ}$ C, f = 250 Hz, No Snubber)	(dv/dt)c	6.5	_	_	A/ms
Critical Rate of Rise of Off–State Voltage (V _D = Rated V _{DRM} , Exponential Waveform, Gate Open, T _J = 125°C)	dv/dt	250	_	_	V/μs

^{*}Indicates Pulse Test: Pulse Width ≤2.0 ms, Duty Cycle ≤2%.

PACKAGE DIMENSIONS



MAC12 SERIES

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MAC12/D